



# Testwell CTC++



Coverage on Host On-Target Coverage for Embedded Systems











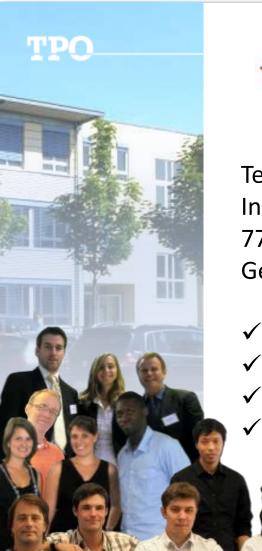


- 1. Verifysoft Short Introduction
- 2. History of Testwell CTC++
- 3. Why Code Coverage?
- 4. Safety Standards and Code Coverage
- 5. Different Coverage Levels
- 6. Compiler Support
- 7. How does it work? Code Instrumentation
- 8. Support for Embedded Targets
- 9. Testwell CTC++ Packages and Qualification Kit
- 10. Different Reports
- 11. Supported Platforms/IDE and Tool Integrations
- 12. Live Demo



# 1. Verifysoft Short Introduction





# Verifysoft TECHNOLOGY

Technologiepark Offenburg In der Spoeck 10-12 77656 Offenburg Germany

✓ Phone: +49 781 127 8118-0 (Germany)

✓ Phone: +33 3 68 33 58 84 (France)

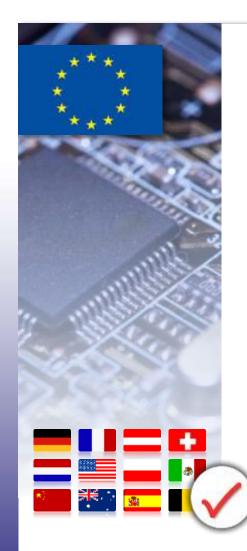
✓ Fax: +49 781 63 920-29

✓ Email: info@verifysoft.com



### 2. History of Testwell CTC++





1989 Start of CTC++ development by Nokia group

1992 Foundation of Testwell Oy, Tampere (Finland) with the mission of further development of CTC++

2003 Foundation of Verifysoft Technology GmbH, Offenburg as distributor for Testwell tools in Europe

2013 Verifysoft purchased Testwell tools

Several hundred CTC++ customers worldwide. More than 1,000 licenses successfully in use. Ongoing development.

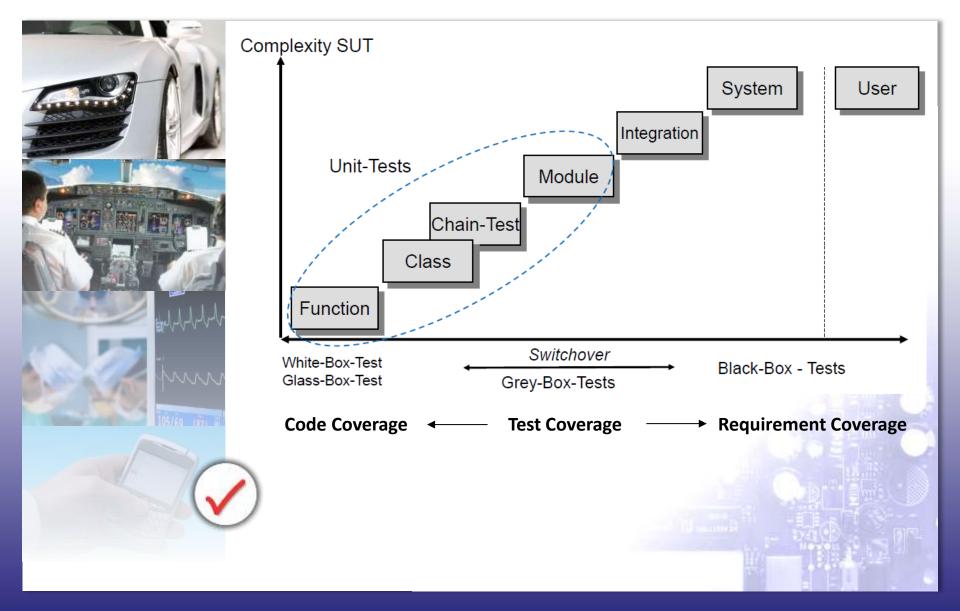
Qualification-Kit

for DO-178C, IEC 61508, EN 50128, ISO 26262



# 3. Why Code Coverage?







# 3. Why Code Coverage?





Cause-Reason-Graph

Classification Tree Method (CTM)

Realtime Testing

**Load Tests** 

Recovery Tests Stress Tests

Control Flow Oriented Testing

**Static Testing** 

Equivalent Classes

Multidimensional Equivalent Classes

Boundary Value Analysis

Critical Value Analysis

Informal Tests

Smoke Tests

Basis

Advanced

Back-to-Back Testing

**CRUD** 

Rare Event Testing

Random Testing

Monkeytest

Fuzzing (Fuzz Testing)

**Evolutionary Testing** 

Pairwise Testing

Established test technique for critical Embedded Systems Test-End criterion (White-Box-Tests)
Necessary to fulfill requirements of safety standards.

Code Coverage: shows the parts of the code which have been

executed / not executed tested / not tested



# 3. Why Code Coverage?



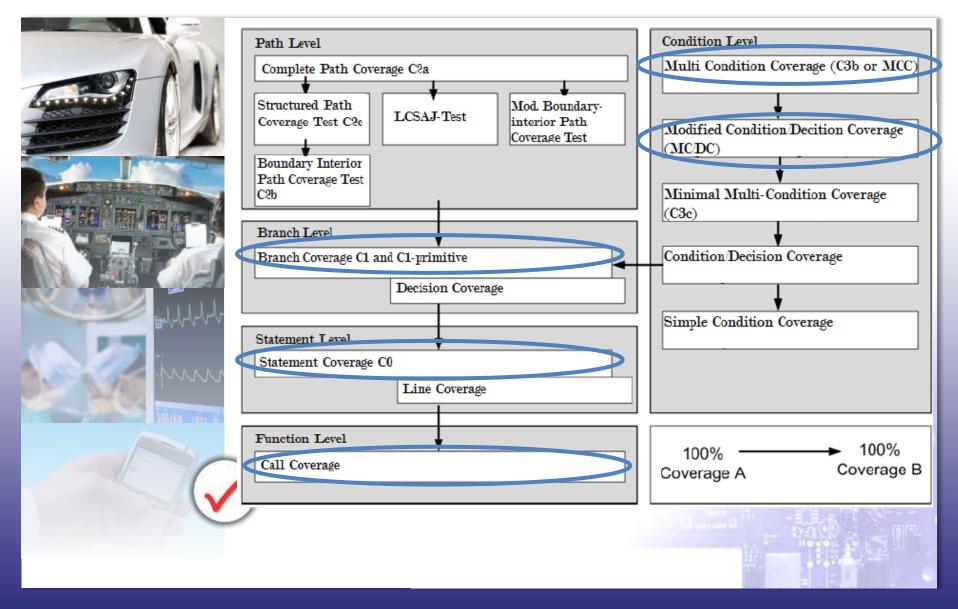


Why measure Code Coverage?

- Write better (more adapted) tests
- Avoid redundant tests
- Know when you can stop testing
- Prove to your customers you have good quality
- Insure that your development partner delivers good quality
- Find Dead Code
- Required to obtain certifications
- Mandatory for safety critical development (standards DO-178C, IEC 61508, EN 50128, ISO 26262, ...)











### **DIN EN 61508-3**

General Industry

SIL: Safety Integrity Level

Method		SIL 1	SIL 2	SIL 3	SIL 4
7a	Function Coverage	++	++	++	++
7b	Statement Coverage	+	++	++	++
7c	Branch Coverage	+	+	++	++
7d	MC/DC	+	+	+	++

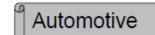
Table B.2 from DIN EN 61508-3

- ++ Highly recommended
- + Recommended





### ISO 26262-6



### ASIL: Automotive Safety Integrity Level

	Methods		ASIL			
		A	В	C	D	
1a	Statement coverage	++	++	+	+	
1b	Branch coverage	+	++	++	++	
1c	MC/DC (Modified Condition/Decision Coverage)	+	+	+	++	

### Table 12 (Software Unit Level), ISO 26262-6

	Methods		ASIL				
		A	В	C	D		
1a	Function coverage	+	+	++	++		
1b	Call coverage	+	+	++	++		

Table15 (Software Architectural Level), ISO 26262-6

- ++ Highly recommended
- + Recommended





### DO-178B/C

Aerospace

Level	Impact	Coverage Level	% of Systems	% of Software
Α	Catastrophic	MC/DC, C1, C0	20-30%	40%
В	Hazardous/Severe	C1, C0	20%	30%
С	Major	CO	25%	20%
D	Minor	-	20%	10%
E	No Effect	-	10%	5%

Statement Coverage C<sub>0</sub>, Branch Coverage C<sub>1</sub>, Modified Condition/ Decision Coverage MC/DC

### IEC 62304

Medical Systems

"... it might be **desirable** to use white box methods to more efficiently accomplish certain tests, initiate stress conditions or faults, or increase code coverage of the qualification tests." (IEC 62304, Chapter B.5.7 Software System testing)



# Code Coverage Requirements: CENELEC EN 50128



Table A.21 - Test Coverage for Code

Technique / measure	Reference	SILO	SIL 1	SIL 2	SIL3	51L 4
Statement		R	HR	HR	HR	HR
Use the Coverage module to report Statement Coverage f and/or target platform.	or the executed Unit Tests and	or monitored	application	runs – on ti	ne host, sim	ulator
Branch		- 21	R	R	HR	HR
	l l		100	177	400000	1000
	rage for the executed Unit Test	s and/or mon	tored appli	cation runs	on the hos	st,
simulator and/or target platform.	rage for the executed Unit Test	s and/or mon	tored appli	cation runs	on the hos	et, HR
Use the Coverage module to report Decision/Branch Cove simulator and/or target platform. Compound Condition Use the Coverage module to report Condition Coverage fo and/or target platform.		<b>1</b> 346	R	R	HR	HR



# 5. Different Coverage Levels





Testwell CTC++ supports all required coverage levels:

- Function Coverage
- Decision Coverage
- Statement Coverage
- Condition Coverage
- Modified Condition/Decision Coverage (MC/DC)
- Multicondition Coverage (MCC)

works together with all unit-test tools



### 6. Compiler Support





### Testwell CTC++ works with all compilers

Support is available for (as of March 2014, for actual list refer to www.verifysoft.com/en compilers.html):

Altium Tasking classic toolsets, VX-toolset toolsets, c166, cc166, ccm16c, cc51

**ARM** DS-5, Keil MDK-ARM

Borland/Inprise/Paradigm/Codegear bcc, bcc32, pcc, pcc32 (Paradigm)

Ceva DSP all (just use gcc settings)

**Cosmic** cx6805, cx6808, cx6812, cxs12x, cxs12z, cxxgate, cx6811, cx6816, cx332, cxst10, cxstm8, cxst7, cxcf, cx56K, cxppc

**Freescale/Metrowerks** mwccmcf, mwcceppc, mwccmcore, mwcc56800, mwcc56800e, chc12, chc08 **Fujitsu/Softune** fcc907s, fcc911s, fcc896s

gcc and all gcc based cross-compilers i586-mingw32msvc-gcc, x86\_64-linux-gnu-gcc, m68k-palmos-coff-gcc, tricore-gcc, arm-linux-gnueabi-gcc, arm-none-eabi-gcc, arm-none-linux-gnueabi-gcc, arm-elf-gcc, arm-montavista-linux-gnueabi-gcc, pic30-gcc, pic32-gcc, avr-gcc, xc16-gcc, mlx16-gcc, thumb-epoc-pe-gcc, arm4-epoc-pe-gcc, armv-epoc-pe-gcc, powerpc-wrs-linux-gnu-e500v2-glibc\_small-gcc, \*-gcc, \*-\*-gcc, \*-\*-\*-gcc

**GHS/GreenHills/Multi** ccv850, cxv850, ccmips, cxmips, ccarm, cxarm, ccthumb, cxthumb, ccppc, cxppc,gcc (GreenHill, not GNU)

Hitatchi shc, shcpp, ch38, cxrx

HI-Tech PICC (Windows and Linux) picc, picc18, picc32, dspicc, xc16-gcc, xc32-gcc,

HP HPUX CC, HP C++, aCC

IAR compilers and toolchains icc430, icc78k, icc78k0r, icc8051, iccarm, iccavr, iccavr32, icccf, icchcs12, iccdspic, iccmaxq, iccpic18, iccr16c, iccv850, icch8, iccm8k, iccm32c, iccm16c, iccr32c, iccr178, iccrx, iccsam8, iccstm8

Intel (all platforms) icc, ic86, ic96

Java compilers Javac, jikes, ecj, gcj, kaffe

**Keil** c51, c166, c251, ca/ cx51, cx2, tcc / armcc

**LLVM** clang, clang++ / Matlab/Simulink / lcc

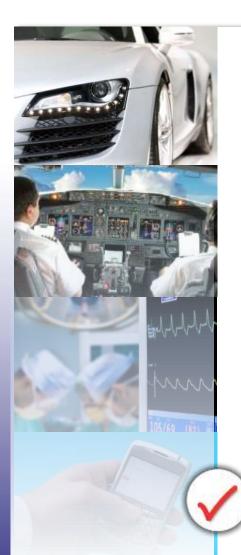
Metaware (both Windows and Linux host) hcac, hcarc, hcarm

Microchip MPLAB C pic30-gcc, pic32-gcc



### 6. Compiler Support





# Testwell CTC++ works with all compilers (continued)



**Microsoft compilers** cl on host, both 32 and 64 bit / cl for Smartphones and PocketPC / csc C# compiler / vic J# compiler

Mono compilers dmcs, mcs, gmcs, smcs

Motorola chc12, chc08

Pathscale pathcc/pathCC

Renesas shc, shcpp, ch38, ccrx

Raisonance rc51, rcmp

**Sun** WorkShop compilers, javac

**Symbian** various compilers

TI Code Composer Studio (Windows) cl2000, cl16x, cl470, cl55, cl500, cl430

**Texas Instruments Linux** compilers cl2000, cl16x, cl470, cl55, cl500, cl430

Trimedia tmcc

VisualDSP++ ccblkfn, cc21k, ccts

**Windriver** ccarm, ccsimpc, g++simpc, g++arm, cchppa, ccsimso, ccsparc, cc68k, cc386, cc960, ccmips, ccppc

You have not seen your compiler? Contact us! We will adapt Testwell CTC++ to your compiler within a few days and without any cost (adaptation can even be done by the customer).

Testwell CTC++ supports <u>all</u> compilers! No unsupported compilers!







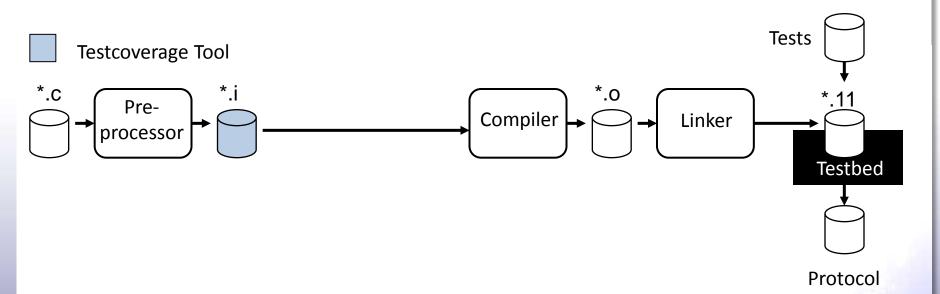
### Instrumentation

- Adding of global counters (Integer-Arrays) into the source code
- Storage of information about counter instrumentation
- Increment counters with each run
- Storage of counter values
- Analysis of the counter values for reporting





### **Tool-Chain**



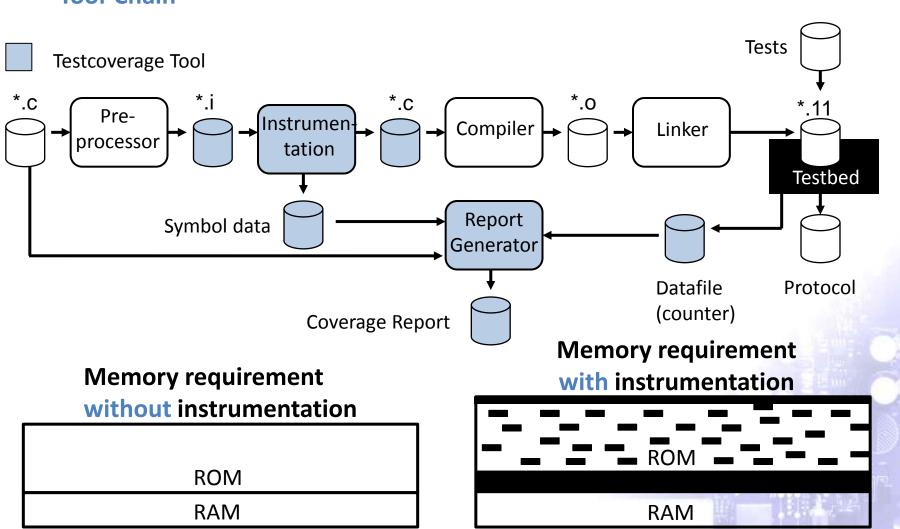
# Memory requirement

ROM
RAM





### **Tool-Chain**

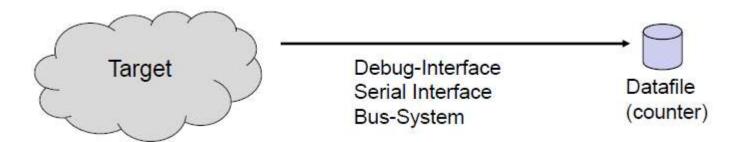




**ROM** 



- RAM Reason for lack of memory: 80 % RAM, 20 % ROM (pract. experience)
- Mostly no filesystem (so counters have to be stored in memory)



 Limited amount of interfaces on the target device (transfer of datafile)
 Consider additional testing interfaces in the hardware design (design for test)





```
int goo( int a, int b, int c)
  int x;
  if (((a>0) || (b>0)) && (c>0))
    x = 1:
  else
    x = 0:
  return x;
```

### **ROM-Usage**

Without instrumentation: 60 Byte Function Coverage: 67 Byte Branch Coverage: 118 Byte Condition Coverage: 285 Byte

Simple example with small code and big instrumentation overhead (mean 30 % of code size).

### Additional RAM-Usage without Bit-Coverage

Function Coverage: 1 Integer Integer:

Branch Coverage: 4 Integer 32 Bit (unsigned long)

Condition Coverage: 7 Integer as default

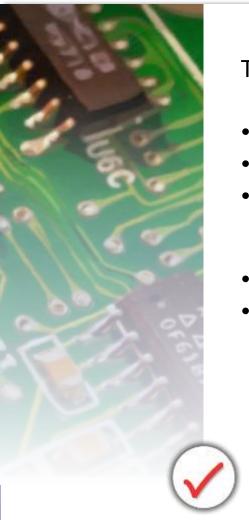
### Additional RAM-Usage using Bit-Coverage

Function Coverage: 1 Bit
Branch Coverage: 4 Bit
Condition Coverage: 7 Bit



# 8. Support for Embedded Targets and native projects





Testwell CTC++ is the ideal tool for embedded targets

- Dramatically easy to use!
- Very low instrumentation overhead on your C files
- Works with all targets
   Host-Target add-on is provided as source code and so can be easily adapted to new targets
- ... even with smallest targets and microcontrollers
- Supports all compilers/cross-compilers



# 8. Support for Embedded Targets and native projects





### **Testwell CTC++** is the ideal tool for native projects

- Setup and usage are straightforward
- Java and C# support on top of C & C++
- Very fast analysis
- Interfacing with MS Visual Studio IDE
- ...even on large projects





# 9. Testwell CTC++ Packages and Qualification Kit





Host-Target add-on for embedded targets

Bit-Coverage add-on for very small embedded targets

Testwell CTC++
Host

CTC++ for Java and Android add-on

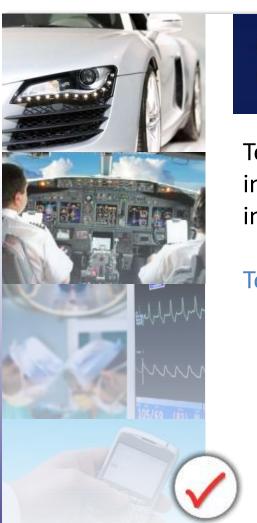
CTC++for C# add-on

You only need one code coverage tool for C, C++,Java and C# One license covers all embedded targets and all compilers



# 9. Testwell CTC++ Packages and Qualification Kit





Compliance with Standards
DO-178C - IEC 61508 - IEC 62304 - ISO 26262

Testwell CTC++ can be used to obtain certification in automotive, railway, avionics and medical industries

Tool-Qualification Kits available















Reports in text, XML, HTML

Directory Summary Files Summary Functions Summary

Execution Profile
Untested Code Listing
Execution Time Listing







### CTC++ Coverage Report - Directory Summary

Directory Summary | Files Summary | Functions Summary | Untested Code | Execution Profile

Symbol file(s) : MON.sym (Mon Feb 17 12:10:50 2014)

: f:\ctcwork\Demos\cube\MON.sym (Fri Mar 14 09:46:50 2014)

Data file(s) : MON.dat (Mon Feb 17 12:13:18 2014)

: f:\ctcwork\Demos\cube\MON.dat (Fri Mar 14 09:47:13 2014)

Listing produced at : Wed Mar 26 14:34:47 2014 Coverage view : Reduced to MC/DC coverage

Input listing : STDIN

HTML generated at : Wed Mar 26 16:34:47 2014 ctc2html v3.5 options: -o webCTCHTML -t 75 -nsb

Threshold percent : 75 %

#### (Click on header to sort)

TER % - MC/DC

#### TER % statement Directory

75 % (21/28) 88 % (21/24) .
66 % - (130/197) 77 % (215/280) f:\ctcwork\demos\cube

67 % - (151/225) 78 % (236/304) OVERALL

Directories : 2 Source files : 7 Functions : 64 Source lines : 905 Measurement points : 221

TER structural : 67 % (151/225) MC/DC

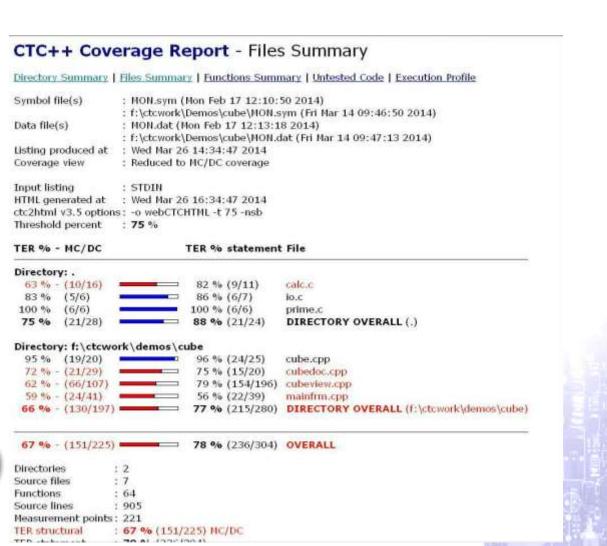
TER statement : **78 %** (236/304)

Directory Summary | Files Summary | Functions Summary | Untested Code | Execution Profile















### CTC++ Coverage Report - Functions Summary #1/2

Directory Summary | Files Summary | Functions Summary | Untested Code | Execution Profile To directories: First | Previous | Next | Last | Index | No Index

#### Directory: .

TER: 75 % (21/28) structural, 88 % (21/24) statement

#### Source file: .\calc.c

Instrumentation mode: multicondition Reduced to: MC/DC coverage

TER: 63 % (10/16) structural, 82 % (9/11) statement

To files: Previous | Next

### TER % - MC/DC TER % statement Calls Line Function

63 % - (10/16)	82 % (9/11)	6	4 is_prime()
63 % - (10/16)	82 % (9/11)		calc.c

#### Source file: .\io.c

Instrumentation mode: multicondition Reduced to: MC/DC coverage

TER: 83 % (5/6) structural, 86 % (6/7) statement

To files: Previous | Next

#### TER % - MC/DC TER % statement Calls Line Function

83 %	(5/6)	86 % (6/7)		io.c
100 %	(2/2)	100 % (1/1)	6	18 io_report()
75 %	(3/4)	83 % (5/6)	8	5 io_ask()

#### Source file: .\prime.c

Instrumentation mode: multicondition Reduced to: MC/DC coverage

TER: 100 % (6/6) structural, 100 % (6/6) statement

To files: Previous | Next

#### TER % - MC/DC TER % statement Calls Line Function

100 %	(6/6)	100 % (6/6)	2	8 main()
100 %	(6/6)	100 % (6/6)		prime.c









### CTC++ Coverage Report - Execution Profile #1/3

Directory Summary | Files Summary | Functions Summary | Execution Profile To files: First | Previous | Next | Last | Index | No Index

File: ./calc.c

Instrumentation mode: function-decision-multicondition

TER: 82 % (14/17)

#### Start/ End/

True False - Line Source

```
1 /* File calc.c ----- */
            2 #include "calc.h"
            3 /* Tell if the argument is a prime (ret 1) or not (ret 0) */
            4 int is prime (unsigned val)
                 unsigned divisor;
                 if (val == 1 || val == 2 || val == 3)
                T | | _ | | _
                F || T ||
                F | | F | | T
                     return 1;
           10
                 if (val % 2 == 0)
           11
                     return 0;
58
           12
                 for (divisor = 3; divisor < val / 2; divisor += 2)
           13
           14
                     if (val * divisor == 0)
           15
                        return 0;
           16
           17
                 return 1;
           18 }
```

\*\*\*TER 82% (14/17) of SOURCE FILE calc.c

<u>Directory Summary | Files Summary | Functions Summary | Execution Profile</u>
To files: <u>First | Previous | Next | Last | Top | Index | No Index</u>







### CTC++ Coverage Report - Execution Profile #1/7

Directory Summary | Files Summary | Functions Summary | Untested Code | Execution Profile To files: First | Previous | Next | Last | Index | No Index

Source file: .\calc.c

Instrumentation mode: multicondition Reduced to: MC/DC coverage

TER: 63 % (10/16) structural, 82 % (9/11) statement

#### Hits/True False -Line Source

```
2 #include "calc.h"
                  1 /* Tell if the argument is a prime (ret 1) or not (ret 0) */
Top
                  4 int is_prime(unsigned val)
                        unsigned divisor;
                        if (val == 1 || val == 2 || val == 3)
                        MC/DC (cond 2): 2 + 4
                       MC/DC (cond 3): 3 - 6
                           return 1;
                       if (val & 2 - 0)
                 10
                           return 0;
                        for (divisor = 3; divisor < val / 2; divisor += 2)
                 13
                           if (val & divisor == 0)
                              return 0;
                       return 1;
```

\*\*\*TER 63% (10/16) of FILE calc.c 82% (9/11) statement

Directory Summary | Files Summary | Functions Summary | Untested Code | Execution Profile To files: First | Previous | Next | Last | Top | Index | No Index







### CTC++ Coverage Report - Untested Code

Directory Summary | Files Summary | Functions Summary | Untested Code | Execution Profile To files: Index | No Index

#### Source file: .\calc.c

Instrumentation mode: multicondition Reduced to: MC/DC coverage TER: 63 % (10/16) structural, 82 % (9/11) statement

#### Hits/True False -Line Source

```
6 4 FUNCTION is prime()

- 8 MC/DC (cond 1): 1 - 4

- 8 MC/DC (cond 3): 0 - 4

0 2 - 12 for (:divisor < val / 2:)

0 0 - 14 if (val 5 divisor == 0)

0 - 15 return 0
```

#### \*\*\*TER 63% (10/16) of FILE calc.c 82% (9/11) statement

#### Source file: ,\io.c

Instrumentation mode: multicondition Reduced to: MC/DC coverage TER: 83 % (5/6) structural, 86 % (6/7) statement

#### Hits/True False - Line Source

8		5	FUNCTION	N io_ask()				
	. 0	11	if ()	amount = 1	eranf (	****	F val	) / <= 0)
***TER 83%	(5/6)	of	FILE io	.c				
86%	(6/7)	st	atement	t				

#### Source file: f:\ctcwork\demos\cube\cube.cpp

Instrumentation mode: multicondition Reduced to: MC/DC coverage TER: 95 % (19/20) structural, 96 % (24/25) statement

#### Hits/True False -Line Source

					CCubeApp:		
	U	1 -	78	if (m	lpCmdLine	1.0.1	THE AVELY
+++TER	95%	(19	(20)	of FILE	cube.cpp		
			/net	stateme			





# 11. Supported Platforms/IDE and Tool Integrations





### **Supported Platforms**

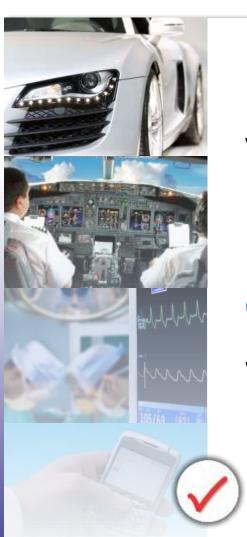
Windows, Linux, FreeBSD, Solaris, HP-UX

MacOSX, AIX, others on request



# 11. Supported Platforms/IDE and Tool Integrations





# **IDE-Integrations**

Visual Studio v7.0 and later IAR (all platforms)
Borland 5.02
Fujitsu Softune
Eclipse
Others on request

Works also with: MP-LAB, Keil, ...



# 11. Supported Platforms/IDE and Tool Integrations





Integrations with Tool-Chains and Testing environments include

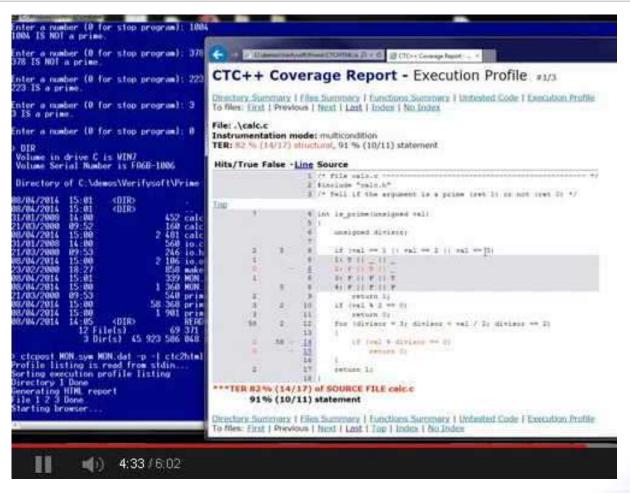
CATIA - AUTOSAR Builder (Dassault Systèmes)
dSpace SystemDesk
dSpace TargetLink
PikeTech TPT
QTronic TestWeaver / Silver
Imagix 4D
SonarQube
... (ask for other integrations)

Further information: www.verifysoft.com



### 12. Live Demo





For an online presentation, please visit http://www.verifysoft.com/en\_ctcpp\_online\_presentations.html







# Testwell CTC++ Test Coverage Analyser for C and C++ CTC++ add-on for Java and Android CTC++ add-on for C#



### All coverage levels

Statement coverage
Function coverage
Decision/branch coverage
Condition coverage
Modified condition/decision cov.
MC/DC coverage
Multicondition coverage (MCC)

All compilers
All embedded targets!
Works with all unit test tools

Compliance with Standards DO-178C - IEC 61508 - IEC 62304 - ISO 26262



### Customers















































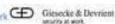




























































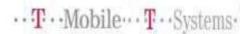








































Testing Solutions

# What can we do for you?

Free tool evaluation incl. support

Testwell CTC++ Training

Further information: www.verifysoft.com



Thank you for your time!

**Your Verifysoft Team**